

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Applicant(s): Narain D. Arora, *et al.*

Examiner: Jimmy Nguyen

Serial No: 10/715,263

Art Unit: 2829

Filing Date: November 17, 2003

Title: NON-CONTACT METHOD AND APPARATUS FOR ON-LINE
INTERCONNECT CHARACTERIZATION IN VLSI CIRCUITSMail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

REPLY TO RESTRICTION REQUIREMENT DATED MAY 4, 2005

This Reply is in response to the Restriction Requirement mailed on May 4, 2005 in connection with the above-identified patent application.

The Examiner requires restriction to one of the following two groups of claims:

Group I – Claims 1-31 and 43-45, drawn to a system that facilitates non-invasive in-line characterization of parameters of VLSI circuit interconnects, classified in class 324, subclass 765.

Group II – Claims 32-42, drawn to a method that facilitates measurement of various parameters of VLSI circuit interconnects, classified in class 324, subclass 757.

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Applicants' representative hereby elects with traverse Group I (Claims 1-31 and 43-45, drawn to a system that facilitates non-invasive in-line characterization of parameters of VLSI circuit interconnects, classified in class 324, subclass 765) for further prosecution on the merits.

Should there be any questions regarding this paper, the Examiner is invited to contact applicants' undersigned representative at the telephone number below.

In the event any fees are due in connection with submission of this document, the Commissioner is authorized to charge such fees to Deposit Account No. 50-1063.

Respectfully submitted,

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